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		2.2	3.3	3.6	VDC	Input to VDD_IO and LDO_VDD pins.
TX	IDD_TXH		20	44	mA	POUT =10dBm
RX	IDD_RX		18		mA	
Idle mode	IDD_IDLE		1		mA	Configured for BRCLK output running.
Sleep mo=	f					



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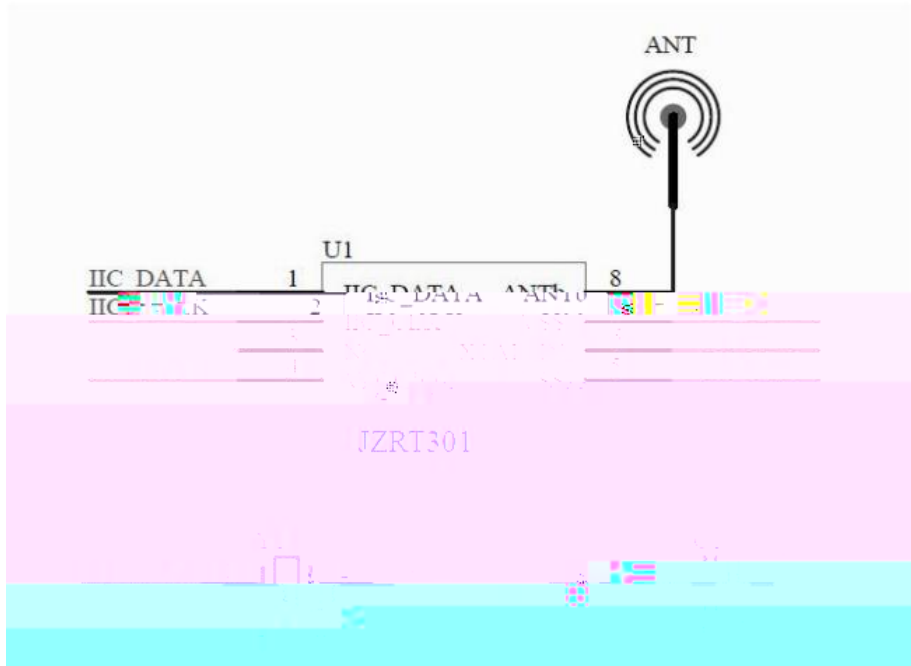
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PLL	FLOCK	2366		2516	MHz	
			--		ppm	Same as XTAL pins frequency tolerance
			1		MHz	
			-95		dBc/H <sub>z</sub>	550kHz offset
			-115		dBc/H <sub>z</sub>	2MHz offset
			24.00		MHz	Designed for 12 MHz crystal reference freq.
	OBS_1		< -75	-57	dBm	30 MHz ~ 1 GHz
	OBS_2		-68	-47	dBm	1GHz ~ 12.75 GHz
						IDLE state, Synthesizer and VCO ON.
	Vdo		0.17	0.5	V	Measured during Receive state



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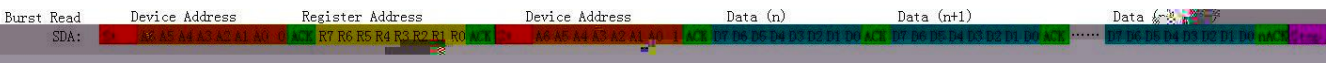
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Start 信号

Stop 信号

器件地址

寄存器地址

数据传输

机给芯片的“0”的应答

机给芯片的“1”的应答

ACK

nACK

Standard-mode – 100 kbps	Yes
Fast-mode – 400 kbps	Yes
Fast-mode Plus – 1000 kbps	Yes
High-speed mode – 2000 kbps	No
Clock Stretching	No
10-bit slave address	No
software reset	No



Bit No.	Bit Name	Description
4		
		PKT
		1: 0
		Fifo

Bit No.	Bit Name	Description
		TX 1 TX_EN RX_EN 1 0 idle
		X 1 TX_EN RX_EN 1 0 idle
6	6	RF f=2402+PLL_CH_NO

Bit No. Bit Name Description  
 | | PA

Bit No.	Bit Name	Description

00B

Bit No.	Bit Name	Description
		reserved
		1: TX/RX VCO 0: TX/RX VCO
		reserved

Bit	Name	R/W	Description	default
15:13	preamble_len	R/W	000: 1byte, 001: 2bytes, 010: 3 bytes, ... 111: 8 bytes	010B
12:11	syncword_len	R/W	11:64bits {Reg39[15:0],Reg38[15:0],Reg37[15:0],Reg36[15:0]}  10:48bits, { Reg27[15:0],Reg26[15:0],Reg25[15:0]}  01: 32bits, { Reg27[15:0],Reg26[15:0]}  00: 16 bits,{ Reg27[15:0]}	11B
10:8	trailer_len	R/W	000: 4 bits, 001: 6bits, 010: 8 bits, 011: 10 bits ... 111: 18bits	000B

5.6



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Bit	Name	R/W	Description	default
15:8	TX			63H
	delay	R/W	VCO_ON PA	07H 1uS

Bit	Name	R/W	Description	default
15	bpktctl_direct	R/W	direct modedede	



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Bit	Name	R/W	Description	default
15:0	SYNC_WORD[31:16]	R/W	1	0000H

Bit	Name	R/W	Description	default
15:0	SYNC_WORD[47:32]	R/W	2	0000H

Bit	Name	R/W	Description	default
15:0	SYNC_WORD[63:48]	R/W	3	0000H

Bit	Name	R/W	Description	default
1			FIFO	00100B
6			FIFO	00100B
			SYNCWORD 07 6bits 01 0bit 0bits	07H

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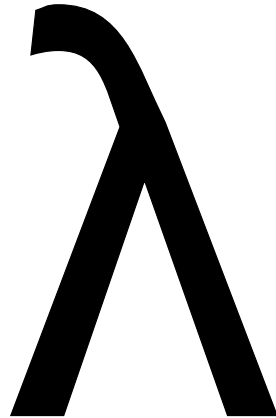
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Name	R/W	Default	Size
crc_on	R/W	1: CRC 0: CRC	1B
14 RESERVED	R/W	RESERVED	0B
13 pack_length_en	R/W	1: payload 8 te	8
12 fw_hw_term_en	R/W		8 9

B





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Bit	a. 17
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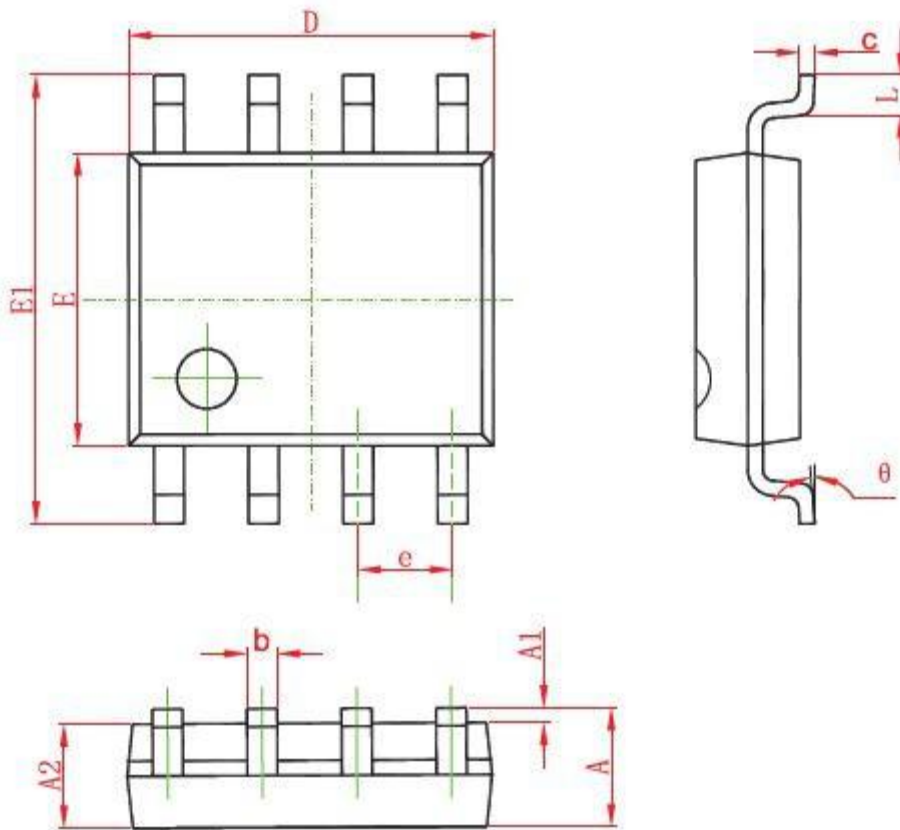
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字符	Dimension In Millimeters	
	Min	Max
A	1.500	1.700
A1	0.040	0.120
A2	1.350	1.550
b	0.300	0.500
c	0.190	0.250
D	4.800	5.000
E	3.840	4.040
E1	5.900	6.100
e	1.27 (BSC)	